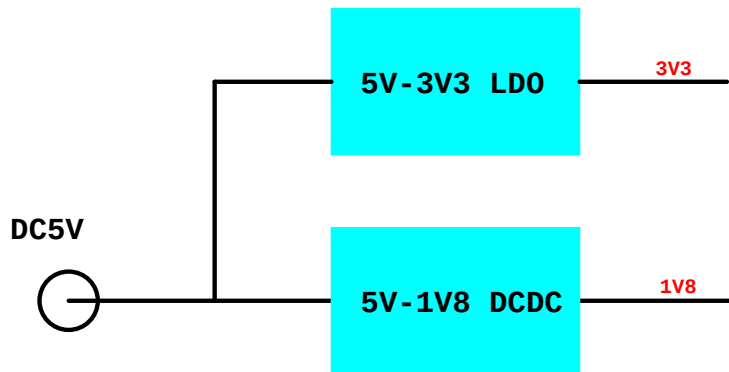
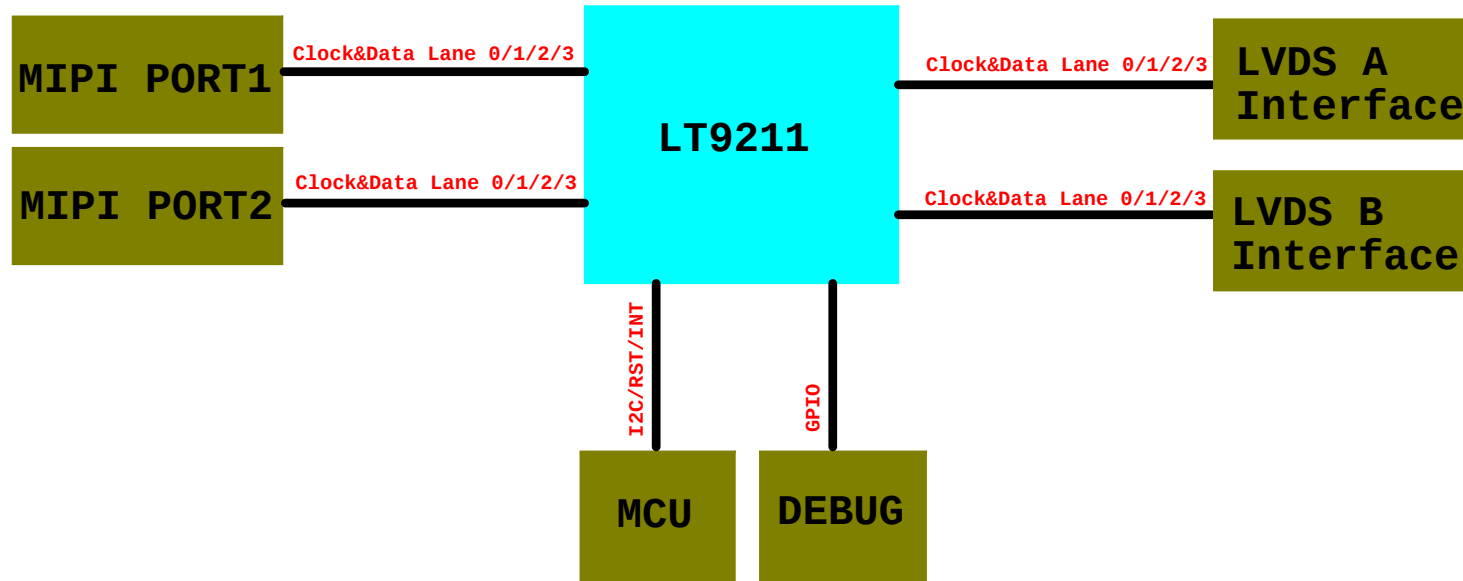


SCHEMATIC	SHEET
PAGE01 : Contents	1
PAGE02 : Block Diagram	2
PAGE03 : LT9211&MCU&POWER	3

Revision History

Date	Version	Comments
2020.03.06	HV1_0	Preliminary release.

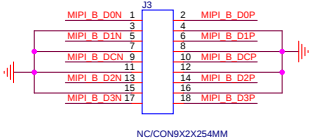
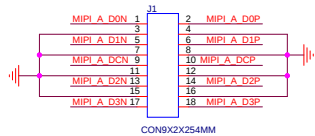
Block Diagram



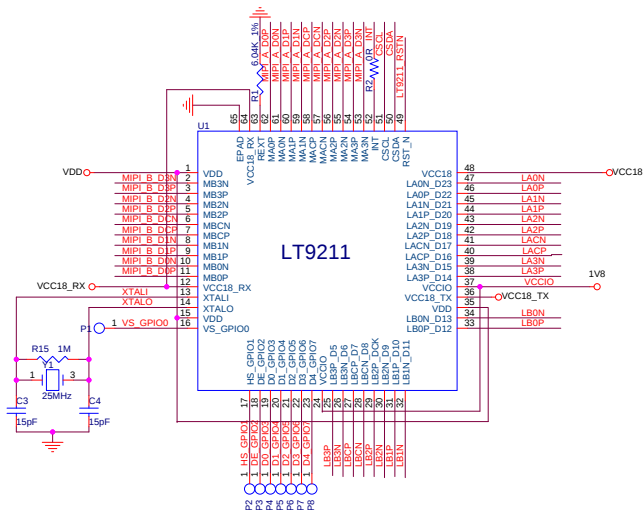
 龙迅半导体（合肥）股份有限公司		
Title Block Diagram		
Size B	Document Number LT9211_MIPI_TO_LVDS_HV1_0	Designer <RevCode>
Date: Tuesday, March 17, 2020	Sheet 2	of 3

MIPI INPUT

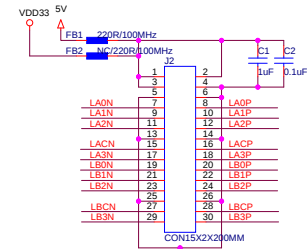
Two PORTs input the same signal, optionally one of the PORTs.



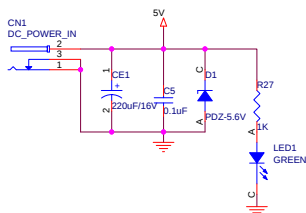
LT9211



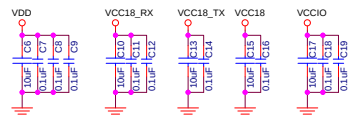
LVDS Output



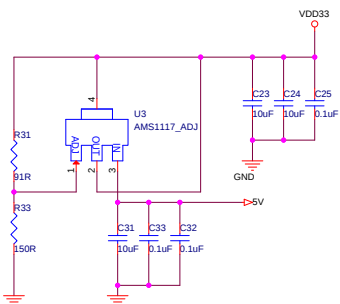
5V_POWER



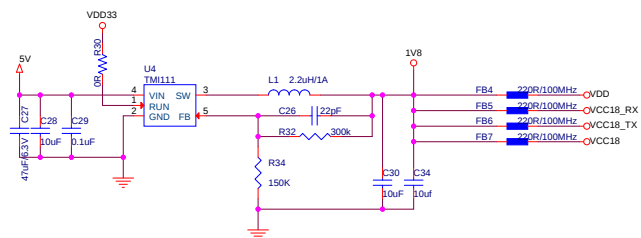
Decouple Capacitance



3.3V POWER



1.8V POWER



MCU

